IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Petra Leber   Docket: DE920000097US1 (8728-729)

Serial No: 09/683,351   Group Art Unit: 2183

Filed: December 18, 2001

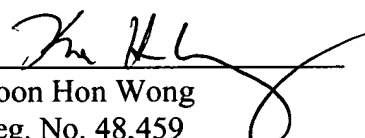For: **METHOD FOR HANDLING 32 BIT RESULTS FOR AN OUT-OF-ORDER PROCESSOR WITH A 64 BIT ARCHITECTURE**

Mail Stop AF
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF FORMAL DRAWINGS

Attached please find eleven (11) sheets of replacement drawings for this application.

Please charge any deficiency as well as any other fee(s) which may become due under 37 C.F.R. § 1.16 and/or 1.17 at any time during the pendency of this application, or credit any overpayment of such fee(s) to Deposit Account No. **50-0510/IBM (Yorktown Heights).** **TWO (2) COPIES OF THIS SHEET ARE ENCLOSED.**
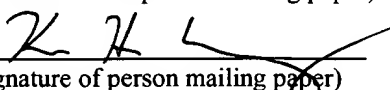
Respectfully submitted

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CERTIFICATE OF MAILING (37 CFR 1.8a)

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Mail Stop AF, Commissioner for Patents, PO Box 1450, Alexandria VA 22313-1450 on **August 8, 2005.**

Koon Hon Wong
(Type or print name of person mailing paper)

(Signature of person mailing paper)

Date: 8-7-05

(Transmittal of Formal Drawings [5-2])